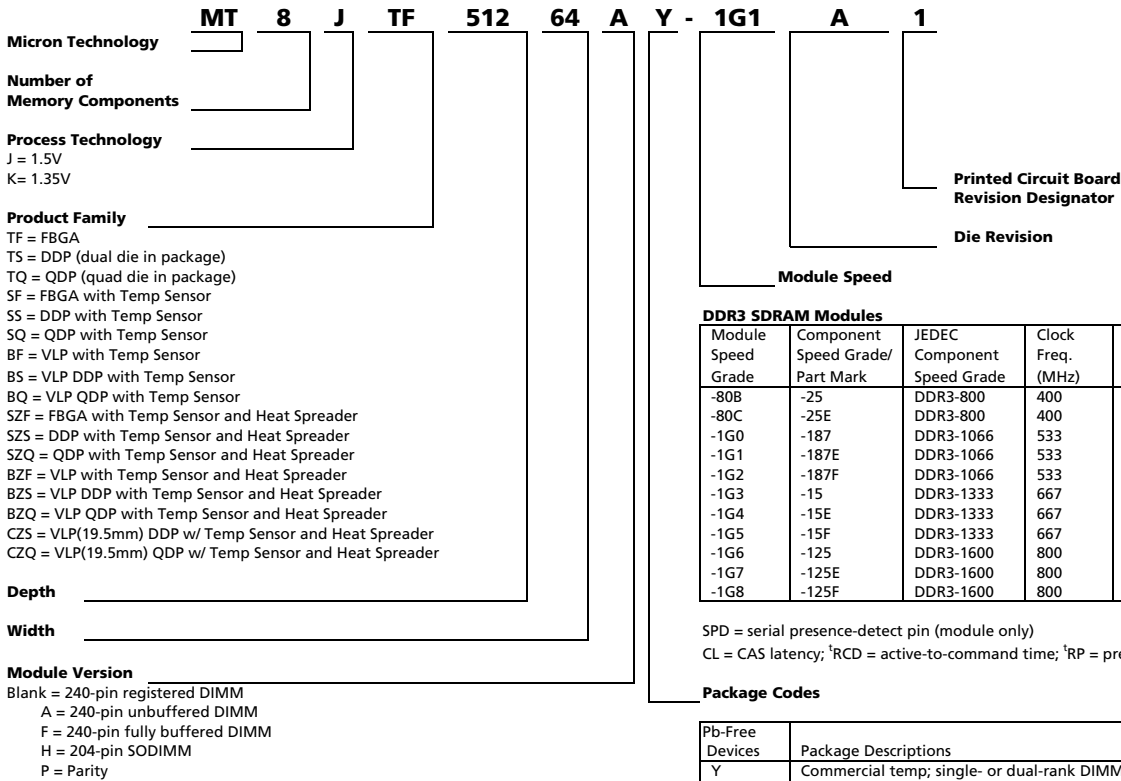


# DDR3 Module Part Numbering System

The part numbering system is available at [www.micron.com/support/designsupport/documents/png](http://www.micron.com/support/designsupport/documents/png)

## DDR3 SDRAM modules



### DDR3 SDRAM Modules

Module Speed	Component Speed Grade/ Part Mark	JEDEC Component Speed Grade	Clock Freq. (MHz)	Data Rate (MT/s)	Module Bandwidth	Module Configuration SPD (CL <sup>1</sup> RCD <sup>1</sup> RP)
-80B	-25	DDR3-800	400	800	PC3-6400	6-6-6
-80C	-25E	DDR3-800	400	800	PC3-6400	5-5-5
-1G0	-187	DDR3-1066	533	1066	PC3-8500	8-8-8
-1G1	-187E	DDR3-1066	533	1066	PC3-8500	7-7-7
-1G2	-187F	DDR3-1066	533	1066	PC3-8500	6-6-6
-1G3	-15	DDR3-1333	667	1333	PC3-10600	10-10-10
-1G4	-15E	DDR3-1333	667	1333	PC3-10600	9-9-9
-1G5	-15F	DDR3-1333	667	1333	PC3-10600	8-8-8
-1G6	-125	DDR3-1600	800	1600	PC3-12800	11-11-11
-1G7	-125E	DDR3-1600	800	1600	PC3-12800	10-10-10
-1G8	-125F	DDR3-1600	800	1600	PC3-12800	9-9-9

SPD = serial presence-detect pin (module only)

CL = CAS latency; <sup>1</sup>RCD = active-to-command time; <sup>1</sup>RP = precharge time

### Package Codes

Pb-Free Devices	Package Descriptions
Y	Commercial temp; single- or dual-rank DIMM
Z	Halogen Free; commercial temp; single- or dual-rank DIMM
DY	Commercial temp; select dual- or quad rank DIMM
DZ	Halogen Free; commercial temp; select dual- or quad rank DIMM

Rev. 9/8/2008

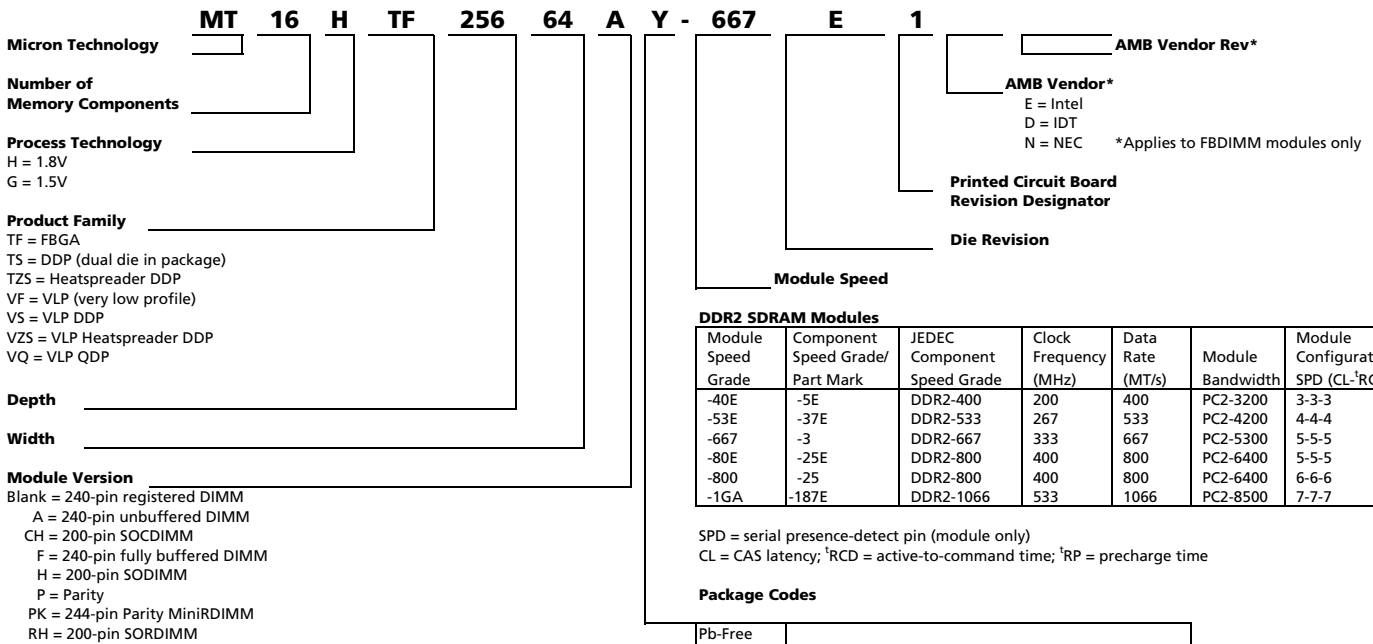
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# DDR2 Module Part Numbering System

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## DDR2 SDRAM modules



### DDR2 SDRAM Modules

Module Speed Grade	Component Speed Grade/Part Mark	JEDEC Component Speed Grade	Clock Frequency (MHz)	Data Rate (MT/s)	Module Bandwidth	Module Configuration SPD (CL <sup>1</sup> -RCD <sup>1</sup> -RP)
-40E	-5E	DDR2-400	200	400	PC2-3200	3-3-3
-53E	-37E	DDR2-533	267	533	PC2-4200	4-4-4
-667	-3	DDR2-667	333	667	PC2-5300	5-5-5
-80E	-25E	DDR2-800	400	800	PC2-6400	5-5-5
-800	-25	DDR2-800	400	800	PC2-6400	6-6-6
-1GA	-187E	DDR2-1066	533	1066	PC2-8500	7-7-7

SPD = serial presence-detect pin (module only)

CL = CAS latency; <sup>1</sup>RCD = active-to-command time; <sup>1</sup>RP = precharge time

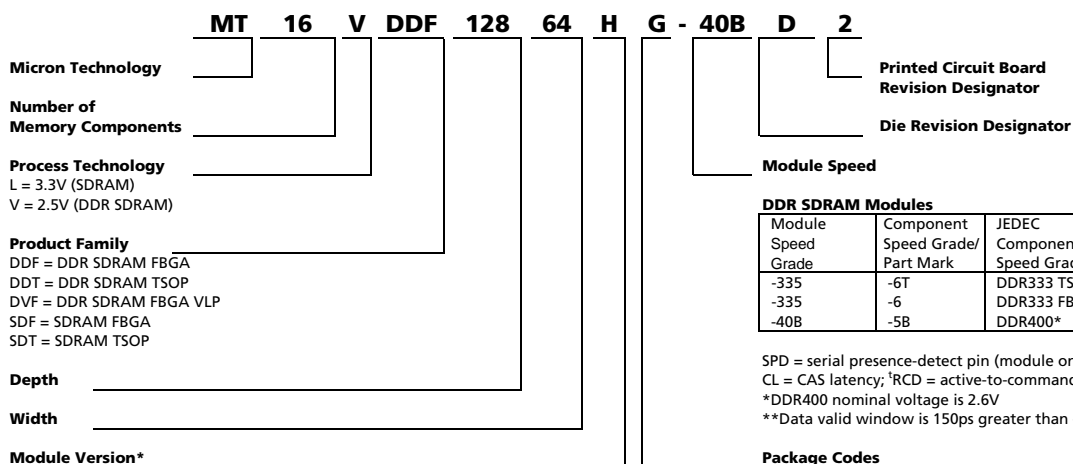
### Package Codes

Pb-Free Devices	Package Descriptions
Y	Commercial temp; single- or dual-rank DIMM
DY	Commercial temp; select dual-rank
IY	Industrial temp; select dual-rank DIMM
TY	Industrial temp; select dual-rank DIMM

# DDR/SDR Module Part Numbering System

The part numbering system is available at [www.micron.com/support/designsupport/documents/png](http://www.micron.com/support/designsupport/documents/png)

## DDR/SDR SDRAM modules



### DDR SDRAM Modules

Module Speed Grade	Component Speed Grade/ Part Mark	JEDEC Component Speed Grade	Clock Frequency (MHz)	Data Rate (MT/s)	Module Bandwidth	Module Configuration SPD (CL <sup>1</sup> -RCD <sup>1</sup> - <sup>1</sup> RP)
-335	-6T	DDR333 TSOP	167	333	PC2700	2.5-3-3
-335	-6	DDR333 FBGA	167	333	PC2700	2.5-3-3**
-40B	-5B	DDR400*	200	400	PC3200	3-3-3

SPD = serial presence-detect pin (module only)  
CL = CAS latency; <sup>1</sup>RCD = active-to-command time; <sup>1</sup>RP = precharge time  
\*DDR400 nominal voltage is 2.6V  
\*\*Data valid window is 150ps greater than -6T

### Package Codes

Lead-Plated Devices	Pb-Free Devices	Package Descriptions
G	Y	Commercial temp; single- or dual-rank DIMM
DG	DY	Commercial temp; select dual-rank
I	IY	Industrial temp DIMM
T	TY	Industrial temp; select dual-rank DIMM

### SDR SDRAM Modules

Module Speed	Allowable Component Speed	CL <sup>1</sup> -RCD <sup>1</sup> - <sup>1</sup> RP
-133	-75, -7E	3-3-3
-13E	-7E	2-2-2

### DDR SDRAM Modules

Module Speed	Allowable Component Speed
-335	-6, -6T, -5B
-40B	-5B

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